

ADL: Architecture Description Language

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```
Integer    $n ::= \dots, -1, 0, 1, 2, \dots$ 
Pointer    $m ::= \ell + n$ 
Value      $v ::= \text{junk} \mid n \mid m ::= \text{Value}$ 
```

図 1: ADL values

Byte Value	$b \in [0, 255]$
Atomic Value	$a ::= \text{junk} \mid b \mid m[n] ::= \text{Atom}$
Data	$d ::= \langle a_0, a_1, \dots, a_{n-1} \rangle ::= \text{Data}$
Memory Block	$k ::= \langle c_0; c_1; \dots; c_{n-1} \rangle$ (Code Block) d (Data Block)
Register File	$R = \{\mathbf{r}_1 \mapsto d_1, \mathbf{r}_2 \mapsto d_2, \dots, \mathbf{r}_N \mapsto d_N\}$
Memory	$M = \{\ell_1 \mapsto k_1, \ell_2 \mapsto k_2, \dots\}$
Temporary Variable	$V = \{A \mapsto v_1, B \mapsto v_2, \dots\}$
Machine State	$S = (R, M, V, m)$

図 2: Storages in the abstract machine

Left Value	$l ::= \mathbf{r}_i[n, n]$	(Register)
	$*[n]e_v$	(Memory Reference)
Expression	$e_v ::= v$	(Literal)
	l	(Left value)
	x	(Variable)
	$e_v op_b e_v op_u e_v$	(Arithmetic)
	$op_b ::= + - * / \% \& ^$	
	$op_u ::= - \sim$	
Boolean Expr.	$e_b ::= e_v cmp e_v$	(Comparison)
	$e_b \wedge e_b e_b \vee e_b$	(Logical Operator)
	$!e_b$	(Negation)
	$cmp ::= == != \dots$	
Command	$c ::= \mathbf{nop}$	(No Operation)
	\mathbf{error}	(Runtime Error)
	$l = e_v$	(Assignment)
	$x = e_v$	(Variable Definition)
	$\mathbf{goto} e_v$	(Jump)
	$\mathbf{if} e_b \mathbf{then} c \mathbf{else} c$	(Conditional)
	$\mathbf{if} e_v : kind \mathbf{then} c \mathbf{else} c$	(Conditional by Kind)
	$kind ::= \mathbf{junk} \mathbf{int} \mathbf{pointer}$	

图 3: ADL syntax