A Generalized Deadlock-Free Process Calculus

Eijiro Sumii Naoki Kobayashi University of Tokyo

Merit and Demerit of Concurrent Languages

Compared with sequential languages...

- Merit: more expressive power
 - Inherently concurrent application (e.g. GUI)Parallel/distributed computation

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Parallel/distributed computation

Demerit: more complicated behavior

Non-determinism (possibility of various results)

Deadlock (failure of due communication)

Merit and Demerit of Concurrent Languages

Compared with sequential languages...
Merit: more expressive power
Inherently concurrent application (e.g. GUI)
Parallel/distributed computation

- Demerit: more complicated behavior
 - Non-determinism (possibility of various results)
 - Deadlock (failure of due communication)

Errors & inefficiencies

Example of Complication (1/2)

In ML:

f: int->int f(3) : int

eventually returns a unique result (unless 'infinite loop' or 'side effect')

Example of Complication (1/2)

```
In CML:
         f: int->int f(3): int
  may return:
     different results in parallel (\rightarrow non-determinism
      fun f(i) =
        let
          val c : int chan = channel()
        in
          (spawn(fn () => send(c, i + 1));
           spawn(fn() => send(c, i + 2));
           recv(c))
```

Example of Complication (1/2)

In CMI : f: int->int f(3): intmay return: different results in parallel (\rightarrow non-determinism no result at all (\rightarrow deadlock) fun f(i) =let val c : int chan = channel() in recv(c) end

Example of Complication (2/2)

Mutex channel m : unit chan
Correct use:
 receive once, send once
 recv(m); CriticalSection; send(m, ())

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Mutex channel **m** : unit chan

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incorrect use:

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receive once, send twice (\rightarrow non-determinism) recv(m); CS; send(m, ()); send(m, ())

Example of Complication (2/2)

Mutex channel **m**, **n** : unit chan

correct use:

receive once, send once

incorrect use:

receive once, send never (→ deadlock)
receive once, send twice (→ non-determinism)
use in various order (→ deadlock)
spawn(fn () => recv(m); recv(n); ...);
spawn(fn () => recv(n); recv(m); ...)

Provide higher-level constructs e.g.:

> parallel functions binary semaphores concurrent objects

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- * "chaos" outside them
- X complicated syntax & semantics

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 x "chaos" outside them
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 Enrich channel types: control communication

 with a static type system

Provide higher-level constructs X "chaos" outside them X complicated syntax & semantics Enrich channel types: control communication with a static type system **Our** approach

Outline

Introduction

- Basic Ideas
- The Type System
- Related Work
- Conclusion

Target Language

Asynchronous variant of Milner's π -calculus

- new x in P (channel creation)
 x![y] (output)
 x?[y].P (input)
- P Q (parallel execution)
 - def x[y]=P in Q (process definition)
- **if x** then **P** else **Q** (conditional branch)

Target Language

Asynchronous variant of Milner's π -calculus new x in P (channel creation) (output) **x!**[y] **x**?[y].P (input) (parallel execution) P | Q def x[y]=P in Q (process definition) **if x** then **P** else **Q** (conditional branch) $x?[y].P | x![z] \otimes P\{z/y\}$ def x[y]=P in x![z]

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Basic Ideas

- Usages & Usage Calculus
 - \Rightarrow "In what way each channel may be used"
- Time Tags & Time Tag Ordering
 - \Rightarrow "In what order those channels may be used"
- The Type System
 - Related Work
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Usages (1/2): Input/Output

- U (usage) :=
 - o (output)
 - **I** U (input + sequential execution)
 - **U V** (parallel execution)
 - (none)
- ✓ x:[]/(0|I)
 - x![] x?[]
- X :[]/(0|I)
 - x![] x![] x?[] x?[]

Usages (1/2): Input/Output

- U (usage) :=
 - o (output)
 - **I** U (input + sequential execution)
 - U V (parallel execution)
 - (none)
- y:[]/(0|0|I.I)
- y![] | y![] | y?[].y?[]
- X y:[]/(0|0|I.I)
 - y![] | y![] | y?[] | y?[]

- U (usage) :=
 - **O**_{*a*} (output)
 - $\mathbf{I}_a \cdot U$ (input + sequential execution)
- a (attributes) :=
 - (none)
 - (obligation: "*must* be performed")
 - **c** (capability: "*can* be performed successfully")
 - co (both)

x:[int]/00

"*must* send an integer value to **x**"

- x:[int]/00 x![3]
- **X** x:[int]/00 0

y:[int]/Ic

"can receive an integer value from **y**

```
successfully"
```

- ✓ y:[int]/Ic 0

What to Ensure:

- An obligation must be fulfilled eventually
- A capability can be used successfully

Otherwise "deadlock"

/ new x:[int]/Ic in x?[v].P

X new x:[int]/Ic in x?[v].P

"For every **I**/**O** with capability, a corresponding **O**/**I** with obligation"

new x:[int]/(Ic|Oo)
 in (x?[v].P | x![3])

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Oo IC IC ® IC

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Dependency between Obligation and Capability

x:[int]/00
 x![3]

 y:[]/I, x:[int]/00
 y?[].x![3]

 y:[]/Ic, x:[int]/00
 y?[].x![3]

Dependency between Obligation and Capability

t<s

"a capability with t may be used before an obligation with **s** is fulfilled"

- y:[]/Ic^t, x:[int]/Oo^s; t<s
 y?[].x![3]
 </pre>
- X y:[]/Ic^t, x:[int]/Oo^s; Æ
 y?[].x![3]
- X y:[]/Ic^t, x:[int]/Oo^s; s<t
 y?[].x![3]</pre>

Preventing & Detecting Cycles in the Dependency

 $G = c:[]/(Oo^{s} | Ic^{s}), d:[]/(Oo^{t} | Ic^{t})$

✓ G; s<t c?[].d![] ...</pre>
X G; s<t d?[].c![] ...</pre>

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Preventing & Detecting Cycles in the Dependency

 $G = c:[]/(Oo^{s}|Ic^{s}), d:[]/(Oo^{t}|Ic^{t})$

X G; s<t c?[].d![] | d?[].c![]
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G; s<t,t<s c?[].d![] | d?[].c![]

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 - Type Judgment & Typing Rules
 - Correctness & Expressiveness
 - Type Checking
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Type Judgment

G; \prec P

G : type environment (mapping from variables to types)

I ≺ : time tag ordering (binary relation on time tags)

P uses communication channels according to:

the usage specified by Γ the order specified by \prec

Example of Typing Rules

T-Out (simplified):

t includes obligations a includes capability
s < time tags on obligations included in t
G includes no obligation

$G + x:[t]/o_a^s + y:t; < x![y]$

```
ret: [int]/00<sup>u</sup>; Æ
  def fib[i:int,r:[int]/00^{s}] =
     if i<2
        then r![1]
         else
            new C:[int]/(Oo<sup>t</sup>|Oo<sup>t</sup>|Ic<sup>t</sup>.Ic<sup>t</sup>)
               in (fib![i-1,c] | fib![i-2,c]
                           | c?[j].c?[k].r![j+k])
  in fib![10,ret]
```

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ret:[int]/Oo<sup>u</sup>; Æ
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Subject reduction: *Well-typedness is preserved by reduction*

No immediate deadlock: *Well-typed processes are not in deadlock*

Subject reduction: *Well-typedness is preserved by reduction* ↓

Deadlock-freedom:

Well-typed processes never fall into deadlock throughout reduction

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Deadlock-freedom: (the case of an output obligation) **G** + $\mathbf{x}:[\mathbf{t}] / \mathbf{O}_{\mathbf{n}}^{\mathsf{t}}; \prec$ Every usage in $\mathbf{G} + \mathbf{x}:[\mathbf{t}] / \mathbf{O}_{\mathbf{t}}^{\mathsf{t}}$ is reliable \prec^+ is a strict partial order \rightarrow **P** will eventually perform output on **x** (unless 'infinite loop')

Expressiveness of the Calculus

Expressive enough to encode:

- Parallel functions
- Typical concurrent objects
- Various semaphores

Expressiveness of the Calculus

Expressive enough to encode:

- Parallel functions
- Typical Concurrent Objects
- Various Semaphores
- Too conservative to express:
 - Case-by-case dependency
 c:[]/(I_{co}^s|0_{co}^s), d:[]/(I_{co}^t|0_{co}^t);
 s<t,t<s
 c![] | d![] |</pre>
 - if ... then c?[]....d?[].... else d?[].....c?[].



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Issues in Type Checking

Usages of channels: must be explicitly specified by programmers Reliability of usages: can be automatically checked (by a co-inductive method) Time tag ordering: can be automatically inferred (by generation & satisfaction of constraints)

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Related Work (1/4)

[Kobayashi 97]

Partially deadlock-free typed process calculus

In what way each channel may be used

- Linear Channels (used just once for communication)
- Mutex Channels (used like binary semaphores)
- Replicated Input Channels (used for process definitio

In what order those channels may be used

Time tags and their ordering

Related Work (2/4)

[Pierce & Sangiorgi 93] I/O Types: In what direction a channel may be used (for input, for output, or for both) c:-[int] c:[int]/!0 [Kobayashi & Pierce & Turner 96] Linear Types: How many times a channel may be used (once or unlimitedly)

 $c \cdot \uparrow^{1}[int] = c \cdot [int]/(0 | T)$

Related Work (3/4)

[Yoshida 96] Graph Types: In what order processes perform input/output on channels

Only 'capability + obligation'; cannot express 'capability without obligation' and 'obligation without capability'

Related Work (4/4)

[Boudol 97] Hennessy-Milner logic with recursion: On what channels processes are ready to receive values

Deadlock-freedom only for output; cannot guarantee deadlock-freedom for input

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Conclusion (1/2): Summary

Static type system that prevents deadlock: Usages & Usage Calculus

"In what way each channel is used"

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Time Tags & Time Tag Ordering "In what order those channels are used"

Conclusion (2/2): Future Work

Develop a (partial) type inference algorithm
Apply to practical concurrent languages
Utilize for compile-time optimization

Prototype type checker available at: http://www.is.s.u-tokyo.ac.jp /~sumii/pub/